

A variable logic cell and a variable analog cell are provided in a zone on a semiconductor chip other than block formation zones of original function circuits. The variable logic cell includes a variable logic circuit for outputting a logic output, a variable wiring circuit for enabling connection of the variable logic circuit with another variable logic circuit or an analog generation circuit, and a wiring-line connection state memory circuit for storing states of switching elements of the variable wiring circuit. The variable analog cell includes an analog generation circuit having a resistive element and a capacitive element for generating a voltage, a variable wiring circuit for connecting the analog generation circuit with another analog generation circuit or variable logic circuit, and a wiring-line connection state memory circuit for storing states of the switching elements of the variable wiring circuit.